# FIG.1
PRIOR ART

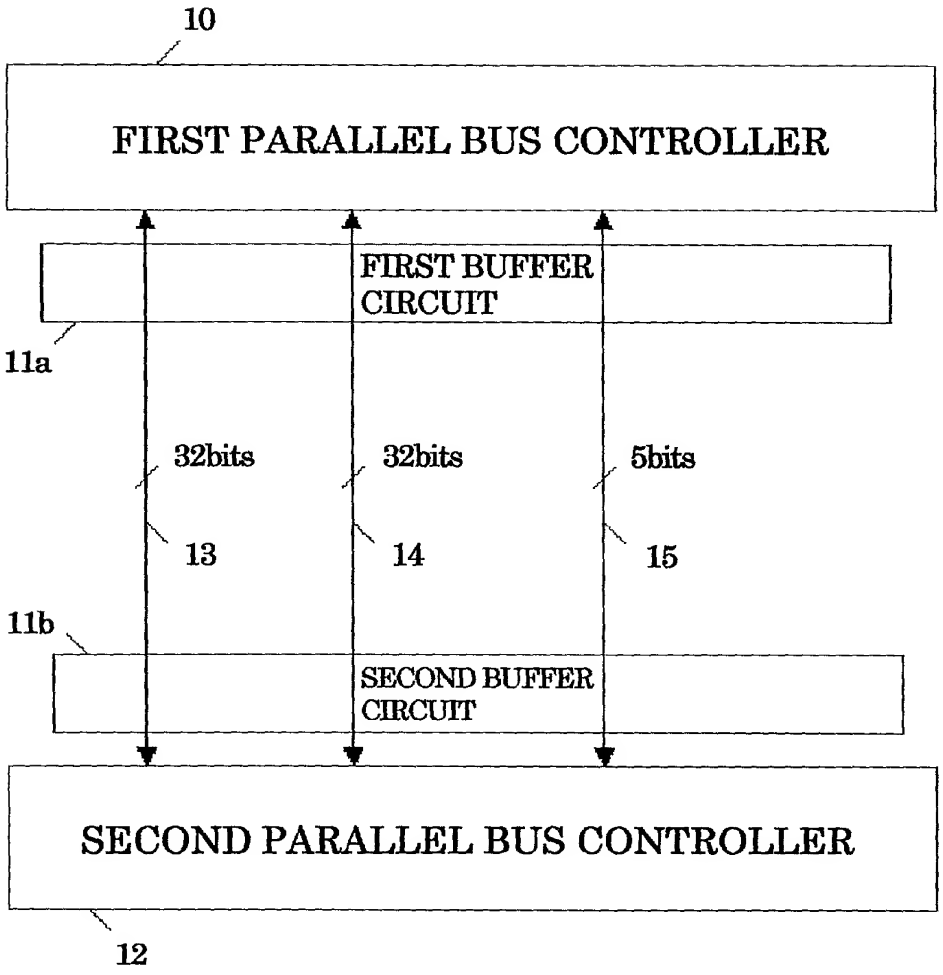
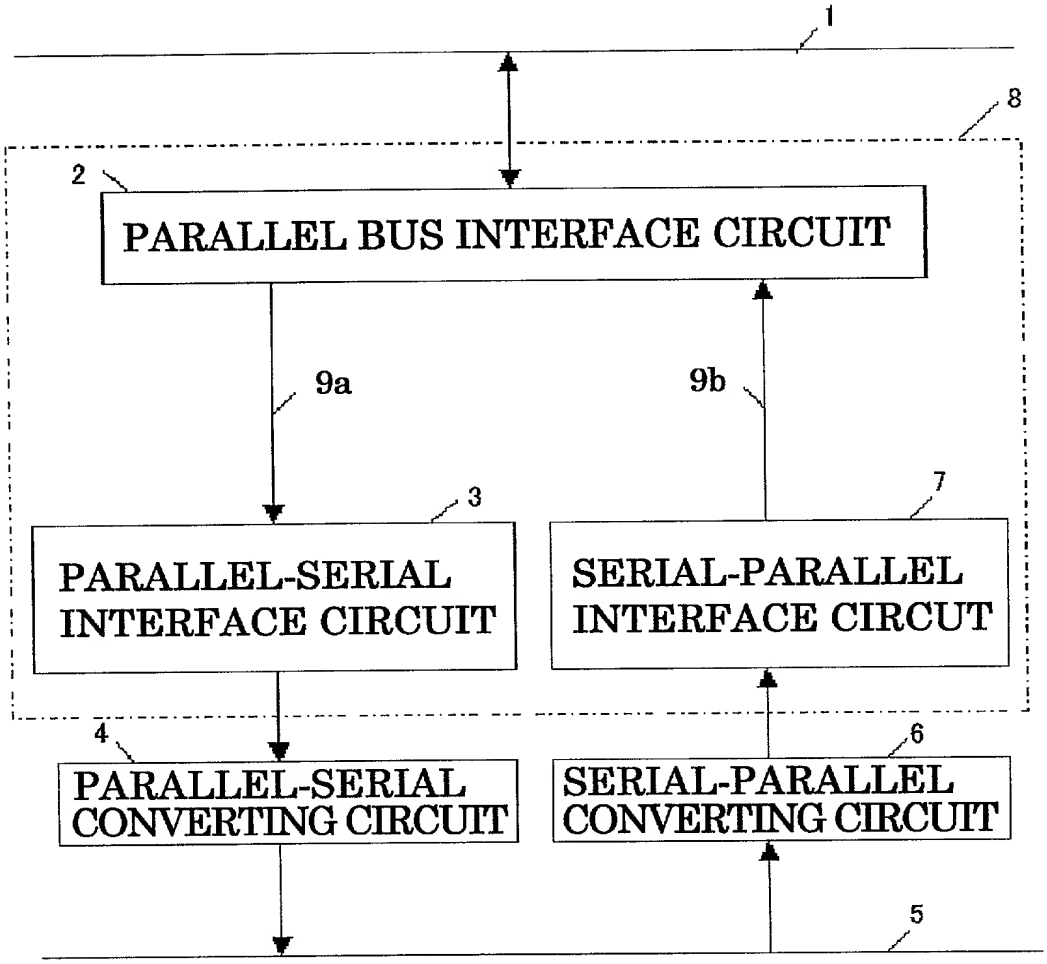
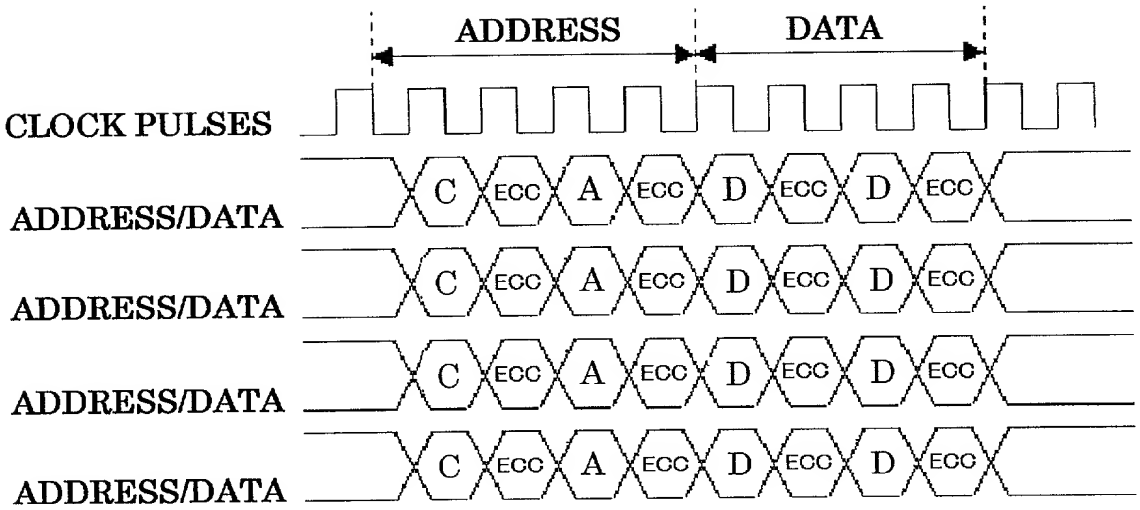


FIG.2


1
8
2
PARALLEL BUS INTERFACE CIRCUIT
9a
9b
3
7
PARALLEL-SERIAL INTERFACE CIRCUIT
SERIAL-PARALLEL INTERFACE CIRCUT
4
6
PARALLEL-SERIAL CONVERTING CIRCUIT
SERIAL-PARALLEL CONVERTING CIRCUIT
5

# FIG.3




C : COMMAND
A : ADRESS
D : DATA
ECC : ERROR CORRECTING CODE